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Major software applications in the list of software development kits for microprocessors, also known as firmware, are listed in a table. Classifications The development process consists of two parts: the design and the verification. According to a standard definition, a digital circuit is of low complexity if it can be designed manually in a short time. According to some standards, such as the IEEE P1500, it also has to be possible to design it within a certain number of electronic design automation tools (EDA). ASIC ASICs are fixed, hard-wired logic circuits that are fabricated on a single silicon wafer. ASICs are most often used to implement a microcontroller. These types of circuits are comparatively low in complexity, inexpensive, and easily programmable. Custom Integrated Circuit In a custom integrated circuit, the design is manufactured by the design house on a custom chip. This can be done either by the manufacture of the chip itself or by the manufacture of the whole custom circuit at a foundry. These are typically used for control purposes, such as microcontrollers. Standard Cell Standard cells are used in foundries and integrated circuits to build a specific function. Standard cells have standard parts that can be connected in different ways to build a design. The process to create a standard cell is called standard cell design. These cells can be used to create macros for instance. Standard cell circuits can be reused in an unlimited number of devices. With the standard cell technology, a designer can build new designs from standard parts to reduce time and costs. RISC RISC was a proposed architecture from the 1980s for microprocessors that has since become obsolete. The proposed architecture was based on the combination of the concept of virtual memory and the idea of microcode. It is defined by five words: Reduced Instruction Set Computer, Reduced Instruction Set, Machine, Instruction Set, Computer. The RISC has several advantages: it simplifies the computer architecture, removes memory management, reduces the number of gates, and it is simple to implement. In the mid-1990s, the term RISCy was used to describe microprocessors with RISC architecture, and xRISC was used to describe RISC-based microprocessors developed for embedded systems. The xRISC project at UC Berkeley was started in the late 1990s, resulting in a processor called the Berkeley RISC Machine. x86 x86 is a 32-bit instruction

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