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cache may both be double-sided and have a latency associated with starting/stopping the clock which may have an impact on the ability to start/stop the clock. Also, some cache memories may require operation during a clock cycle and some other cache memories may require operation during an inactive clock cycle. A startup/shutdown is a method by which a synchronous device, such as a circuit, begins or stops operation, depending on whether a synchronization signal has been asserted. {
"jsonSchemaSemanticVersion": "1.0.0",



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3/6

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